



Farapayam Engineering Co.

## Line trap

PLC Transmission System



line traps are designed to prevent the carrier frequency signals from traveling in undesired directions of the frequency bands. They are connected in series with high - voltage lines of substations and of the feeder locations; they must be designed to withstand they maximum continuos current as well as any short circuit current that may occur at the location.

Line traps provide defined impedance and attenuation regardless at the circuit state of the high-voltage network.



Line trap / 1mH / 2000 A / 50 KA  
KEMA - high power lab.

## APPLICATION

► High voltage lines are used to transmit various signals such as telemetry signals, remote control signals, telephone calls, telex messages, etc. In this connection,

[www.Farapayamco.com](http://www.Farapayamco.com)



Transducer



Iron Core Reactor



Air Core Reactor



L.M.U



## ADVANTAGES OF THE FARAPAYAM DESIGN

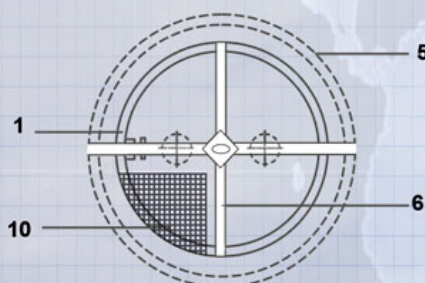
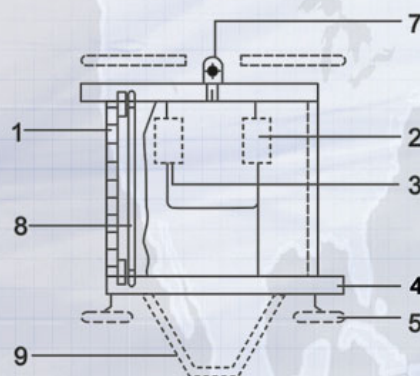
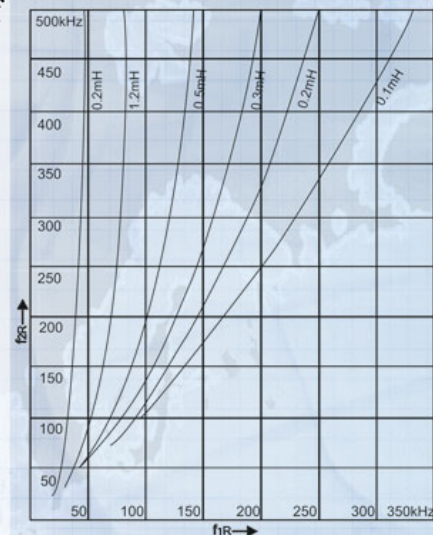
- Exceptional short circuit withstand ability for greater operational reliability
- Negligible inductivity tolerance
- Low capacitance of the main coil
- Higher Q
- Smaller volume
- Open struture permits excellent cooling of the live conductor
- Lower mass
- No maintenance required

## Graph for determination of band limit frequencies

$f_{1R}$  and  $f_{2R}$  with a minimum resistive impedance  $R_b = 600\Omega$  ( other impedance values are possible, smaller values result in broader bandwidth and vice versa)

### Example :

$f_{1R} = 116 \text{ KHZ}$ ,  $f_{2R} = 272 \text{ KHZ}$  for  
 $f_{1R}$  = Lower bandlimit frequency  
 $f_{2R}$  = Upper bandlimit frequency  
 $LtN = 0.5 \text{ mH}$



- 1 = Mian coil
- 2 = Tuning device
- 3 = Protective device
- 4 = Terminals
- 5 = Corona guard ring (optional)
- 6 = Spider
- 7 = Lifting eye
- 8 = Tie bars
- 9 = Support for pedestal mounting
- 10 = Bird barrier (optional)

## TESTS

All type tests recommended by the IEC-353 are carried out in the **KEMA, CESI & VVA** laboratory. Basically all routine tests are carried out on every FARAPAYAM line trap.

## Circuit diagram and blocking characteristics of a band tuned line trap

$LtN$  = Main inductance, measured at 100KHZ

$C1, C2, R_d, L2$  = Tuning elements

$|Z_b|/R_d$  = Normalized impedance characteristics

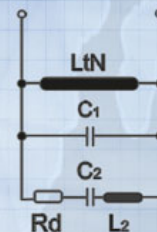
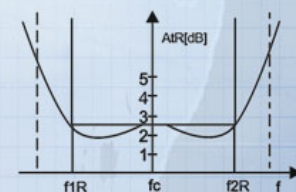
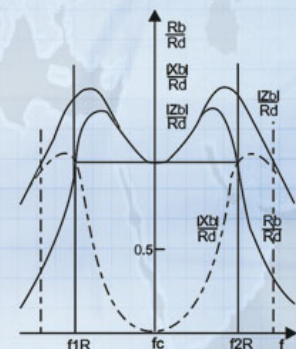
$R_b/R_d$  = Normalized characteristics of resistive components

$|X_b|/R_d$  = Normalized characteristics of resistive components

$A_{tR}$  = Tapping loss based on blocking resistance

$f_{1R}, f_{2R}$  = Cut - off frequencies

$f_c$  = Center frequency



info@Farapayamco.com

Designed by FormAsa (0261 448 81 84)

www.Farapayamco.com Hamdavi@Farapayamco.com Info@Farapayamco.com

Office : Unit 12, Bahram bldg., No 4, 32th St., Gandhi St., Tehran - Iran  
 Factory : No 175, Sixth Golestan alley, Baharestan industrial city, Kamalshahr, Karaj - Iran  
 Office Telefax : ( +98 21 ) 88208436 - 7      Factory Telefax : ( +98 261 ) 4760477 - 9